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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF : :

NAOKI TSUKIJI ET AL. : GROUP: 2813

SERIAL NO: 10/014,507 : EXAMINER: L. SCHILLINGER

FILED: DECEMBER 14, 2001 : :

FOR: METHOD OF MANUFACTURING SEMICONDUCTOR ELEMENT, AND
SEMICONDUCTOR ELEMENT MANUFACTURED WITH THIS METHOD

RESPONSE TO RESTRICTION/ELECTION REQUIREMENT

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

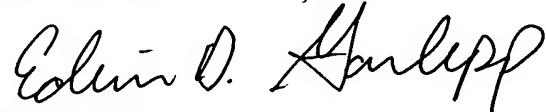
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In response to the Restriction Requirement dated May 12, 2003, Applicants provisionally elect Group I, Claims 1-36, directed to a method of making a semiconductor device, and elect Species I, Claims 1-16, without traverse.

Accordingly, full examination on the merits of Claims 1-16 is requested.

Respectfully submitted,

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